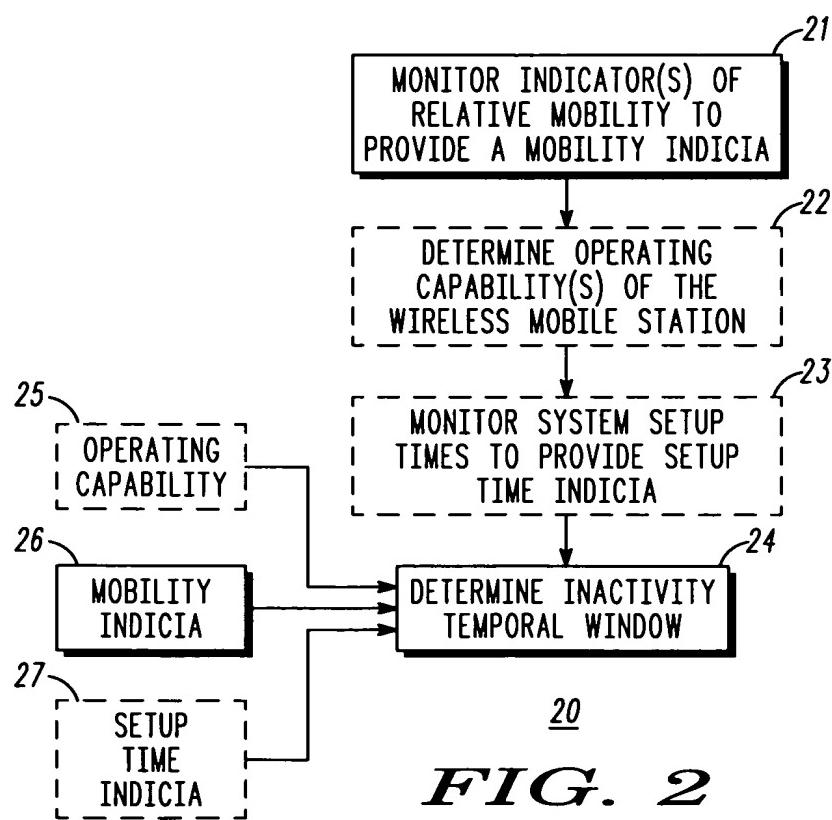
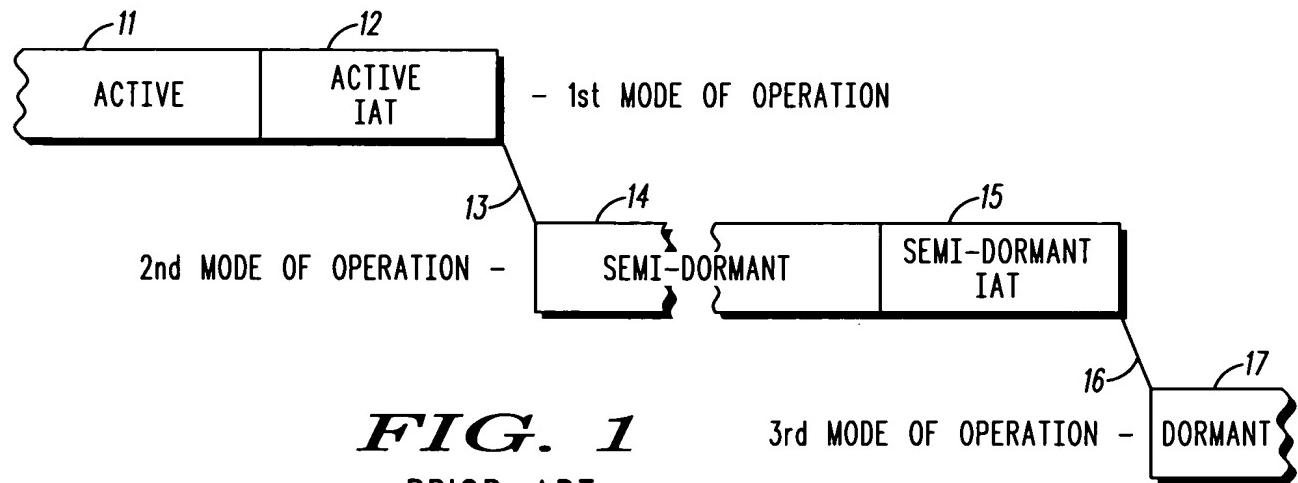
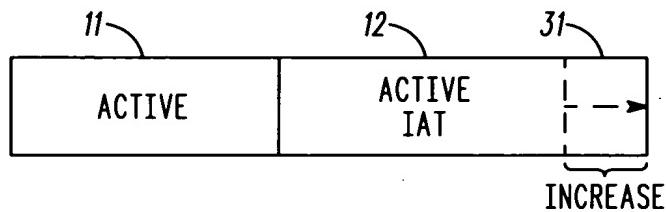


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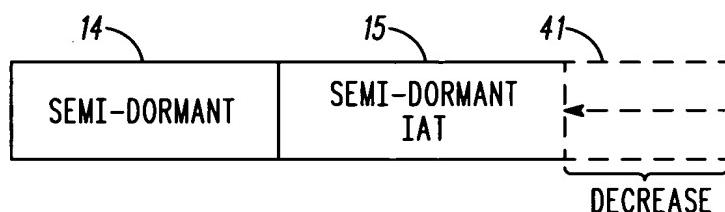


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**FIG. 3**



**FIG. 4**



53  
MEMORY  
MOBILITY  
INDICIA  
INFORMATION

51  
PROCESSING  
PLATFORM

52  
MEMORY  
INACTIVITY  
TIMER  
INFORMATION

50

54  
MEMORY  
SETUP DELAY  
INFORMATION  
OPERATING  
CAPABILITY  
INFORMATION

**FIG. 5**